Apple II and IIe: Interface I/O Signal Timing (2/97)

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Security: Everyone

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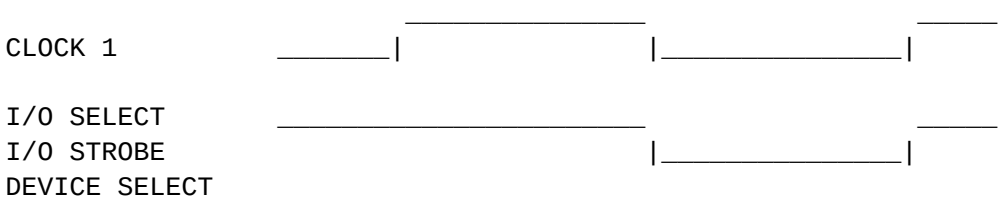
TOPIC

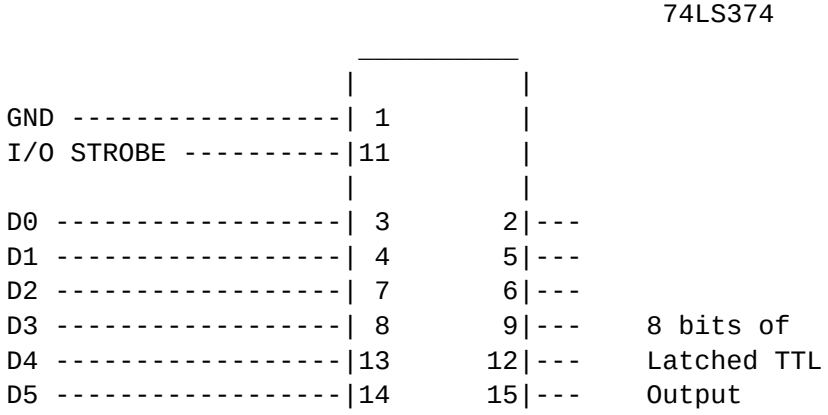
This article describes how the I/O strobe signals on the Apple II peripheral connector are handled.

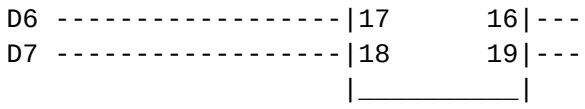
DISCUSSION

The I/O strobe signals on the Apple II peripheral connector are decoded from the appropriate address lines and combined with the phase one clock. This is to reduce the TTL circuitry required to build a simple I/O port.



A simple 8 bit output port would be a positive edge triggered latch with the clock tied to I/O select.





Assumming that this interface is plugged into slot 1, any write operation to $C090..$C09F will transfer the data to the output lines. This is a very simple interface, so any read to $C090..$C09F will transfer random data to the output latch and to the Apple.

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